AM/FM 1 CHIP RADIO S1A0426C02

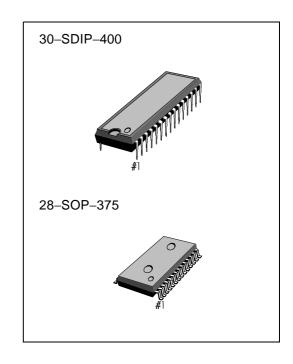
#### INTROCUCTION

The S1A0426C02 is a monolithic integrated circuit designed for radio cassette tape recorders, clock radios and headphone radios.

#### **FUNCTIONS**

- AM/FM RF AMP
- AM AGO Control
- Audio Power AMP
- DC Volume
- FM Quadrature DET
- AUDIO MUTE

- Local OSC
- FM AFO Control
- Tuning Indicator
- AM/FM IF AMP
- AM DET



### **FEATURES**

- Built-in AM/FM Switching Circuit
- Wide operating supply voltage: V<sub>CC</sub> = 2V 8.5V
- Low current consumption (V<sub>CC</sub> = 3V)
  - FM:  $I_{CCQ} = 5.3 \text{ mA (Typ)}$
  - AM:  $I_{CCQ} = 3.4 \text{ mA (Typ)}$
- High Power Audio Amplifier: 0.5W (typ) at V<sub>CC</sub> = 6V,
- RL =  $8\Omega$ , THD = 10%

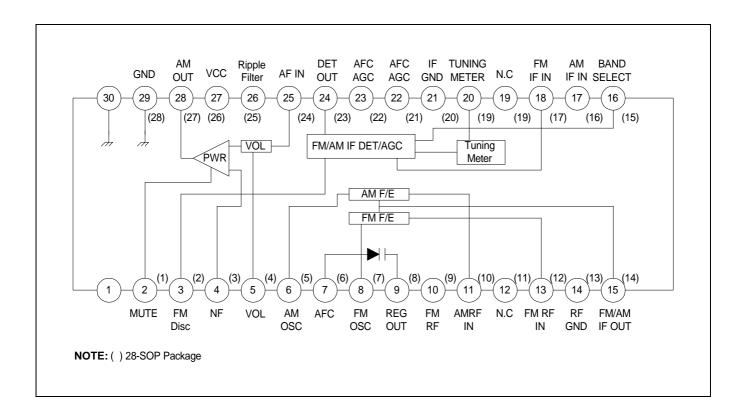
### **ORDERING INFORMATION**

Device	Package	Operating Temperature			
S1A0426C02-A0B0	30-SDIP-400	−20°C − +70°C			
S1A0426C02-S0B0	28-SOP-375	−20°C − +70°C			



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#### **BLOCK DIAGRAM**



# **ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)**

Characteristic	Symbol	Value	Unit
Supply Voltage	V <sub>CC</sub>	9	V
Power Dissipation	P <sub>D</sub>	1000	mW
Operating Temperature	T <sub>OPR</sub>	-20 - +70	°C
Storage Temperature	T <sub>STG</sub>	-40 - <b>+</b> 125	°C

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# **ELECTRICAL CHARACTERISTICS**

(V<sub>CC</sub> = 6 V, Ta = 25 °C, FM; Δf = 22.5 kHz, fm = 1 kHz, AM; 30% Mod, unless otherwise specified)

	Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
FM	Quiescent Circuit Current	I <sub>CCQ</sub> (1)	V <sub>I</sub> = 0	_	7.0	14.0	mA
	F/E Voltage Gain	G <sub>V</sub> (1)	$V_I(1) = 40 dB\mu$ , $fc = 100 MHz$ , $\Delta f = 0$	32	39	46	dΒμ
	Detect Output Gain	V <sub>O</sub> (1)	$V_I(3) = 90dB\mu$ , fi = 10.7MHz	-26	-20	-14	dBm
	IF-3 dB Sensitivity	V <sub>I (LIM)</sub>	$Vo(VI3) = 90dB\mu, -3dB, fi = 10.7MHz$	_	24	32	dB
	Total Harmonic Distortion	THD <sub>1</sub>	$V_I(3) = 90dB\mu$ , fi = 10.7MHz ( $\Delta f = 75kHz$ )	_	0.3	2.0	%
	Meter Drive Current	I <sub>M</sub> (1)	$V_I(3) = 60 dB \mu$ , fi = 10.7MHz	1.8	3.5	7.0	mA
АМ	Quiescent Circuit Current	I <sub>CCQ</sub> (2)	V <sub>I</sub> = 0	-	3.5	10.0	mA
	F/E Voltage Gain	G <sub>V</sub> (2)	$V_I(2) = 60 dB\mu$ , fc = 1660kHz, m = 0%	15	22	29	dB
	IF Voltage Gain	G <sub>V</sub> (3)	Vo(3) = -34dBm, fi = 455kHz	14	20	27	dΒμ
	AM Detect Output Voltage	V <sub>O</sub> (2)	$V_{I}(3) = 85dB\mu$ , fi = 455kHz	-26	-20	-14	dBm
	Total Harmonic Distortion	THD <sub>2</sub>	$V_I(2) = 95 dB\mu$ , fc = 1660kHz, Vcc = 7.8V	_	0.6	2.0	%
	Meter Drive Current	I <sub>M</sub> (2)	$V_{I}(3) = 85 dB\mu$ , fi = 455kHz	1.3	3.0	7.0	mA
AF	Closed Loop Voltage Gain	G <sub>V</sub> (4)	Vo(4) = 0dBm, f = 1kHz	27	31.5	36	dB
	Total Harmonic Distortion	THD <sub>3</sub>	Po = 50mW, f = 1kHz	_	0.3	2.5	%
	Output Power	P <sub>O</sub>	$R_L = 8\Omega$ , THD = 10%, f = 1kHz	0.4	0.5	_	W
	Mute Level	$M_L$	Po = mW, Vi(4) = 30dBm 1kHz, V1(3) = FF	8	15	22	dB



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# **APPLICATION CIRCUIT**

